

AUTUMN EXAMINATIONS 1999

3rd year B.Sc. Unit EP326: Solid State Physics

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Time allowed: TWO hours.

Answer THREE questions

*Physical data for silicon at 300 K, and other constants**(a) Silicon at 300 K:*

$$N_C = 2.8 \times 10^{25} \text{ m}^{-3}$$

$$\mu_n = 0.14 \text{ m}^2 \text{ V}^{-1} \text{ s}^{-1}$$

$$D_n = 36 \times 10^{-4} \text{ m}^2 \text{ s}^{-1}$$

$$n_i = 1.45 \times 10^{16} \text{ m}^{-3}$$

$$\text{Relative dielectric constant of Si, } \epsilon_s = 11.9$$

$$\text{Density of Si atoms (crystalline)} = 5.0 \times 10^{28} \text{ atoms m}^{-3}$$

$$\text{Resistivity of intrinsic Si} = 2300 \text{ } \Omega \text{ m}$$

(b) Other constants:

$$q = 1.602 \times 10^{-19} \text{ C}$$

$$h = 6.626 \times 10^{-34} \text{ J s}$$

$$\text{Relative dielectric constant of SiO}_2, \epsilon_{\text{ox}} = 3.9$$

$$N_V = 1.04 \times 10^{25} \text{ m}^{-3}$$

$$\mu_p = 0.05 \text{ m}^2 \text{ V}^{-1} \text{ s}^{-1}$$

$$D_p = 13 \times 10^{-4} \text{ m}^2 \text{ s}^{-1}$$

$$E_G = 1.12 \text{ eV}$$

$$kT = 0.0259 \text{ eV at } T = 300\text{K}$$

$$m_e = 9.11 \times 10^{-31} \text{ kg}$$

$$\epsilon_0 = 8.85 \times 10^{-12} \text{ F m}^{-1}$$

Q.1 Write full notes on any *two* of the following topics.

- (a) The concepts of electron energy bands and holes in semiconductors.
- (b) The variations, with temperature T , of the carrier concentrations in a doped semiconductor.
- (c) The transport and continuity equations in semiconductors.

Q.2 Define the terms in, and explain the physical significance of, the two semiconductor equations given below. What is the essential condition for these equations to be valid?

$$n = N_C \exp\left[\frac{-(E - E_c)}{kT}\right]$$

$$N_D^0 = 2N_D \exp\left[\frac{-(E_D - E_F)}{kT}\right]$$

Silicon is doped with phosphorous impurity atoms at a concentration of $1.5 \times 10^{22} \text{ atoms m}^{-3}$. Calculate the accurate percentage ionisation of the phosphorous atoms, the conductivity μ of the doped silicon, and the position of the Fermi level, $E_C - E_F$, in the silicon. Is the doped silicon degenerate or non-degenerate?

Note: $E_C - E_D = 0.049 \text{ eV}$ for P doping in Si.

- Q.3 By estimating the minority drift current, J_{PF} , of holes across a semiconductor p-n junction, show that the term J_o in the ideal diode equation, $J = J_o[\exp(qV/kT) - 1]$, can be written as:

$$J_o = \frac{q D_p p_{na}}{L_p} + \frac{q D_n n_{pa}}{L_n} \approx \frac{q D_p n_i^2}{L_p N_D} + \frac{q D_n n_i^2}{L_n N_A}$$

Explain the meanings of all the terms used in this equation. Under what assumptions is the approximation used in the second half of the equation valid?

The minority carrier lifetimes in the p and n regions of a p-n junction diode are 120 μ s and 50 μ s, respectively. The respective doping levels are $N_A = 2.5 \times 10^{23} \text{ m}^{-3}$ and $N_D = 1.5 \times 10^{21} \text{ m}^{-3}$. Calculate the minority diffusion lengths in both regions, and estimate the percentage of hole current to total current across the junction. Why, and where, is it important to be able to control this ratio in the fabrication of a bipolar junction transistor?

- Q.4 Briefly describe, and sketch, the modern day (planar epitaxial) fabrication method for an npn Bipolar Junction Transistor (BJT). Explain how this fabrication technique produces a *drift transistor* with built in electric field in the base. What is the main BJT operational benefit of this electric field? Define the *grading factor* η for such a device.

An npn BJT has a base width $W = 1.2 \mu\text{m}$, and a base grading factor $\eta = 2.5$. Calculate the drift velocity of electrons across the base, due to the built in field. Hence calculate the electron base transit times due to drift alone, and also due to diffusion alone. Define and estimate the *transition frequency*, f_T , for this transistor.

- Q.5 Define the *threshold voltage*, V_{TH} , of a MOSFET device. State the equation, which is used to determine V_{TH} , explaining the origin and physical significance of each of the four terms that go to make up the equation. In addition, state briefly how the *surface states* arise.

A silicon NMOSFET device, with an oxide thickness of 60 nm, has its threshold voltage measured as 0.25 V. The theoretical threshold voltage in the absence of any surface states is calculated as being 0.95 V. Calculate the surface charge, Q_{ox} , and numerical density, N_{ox} , of the surface states. Say clearly whether this is a positive or negative charge density. The threshold voltage is to be adjusted to $V_{TH} = 1.2 \text{ V}$, during fabrication, by a boron ion implant in the channel. Estimate the ion implant dose, N_i , required.